

Serial No. 10/776,338

Attorney Docket No. 01-549

**REMARKS**

Claims 1-19 were pending. Claims 10-19 have been canceled herein without prejudice or disclaimer due to restriction. Thus, claims 1-9 are now pending. Claims 4, 6, and 7 are withdrawn pending rejoinder. The applicants respectfully request reconsideration and allowance of this application in view of the above amendments and the following remarks.

The applicants note with appreciation the acknowledgement of the claim for priority under section 119 and the notice that all of the certified copies of the priority documents have been received.

The applicants acknowledge and appreciate receiving an initialed copy of the form PTO-1449 which accompanied the Information Disclosure Statement that was filed on February 12, 2004.

Claim 2 is amended herein for clarity and not for reasons related to patentability.

Applicants request rejoinder of claim 4, 6 and 7, which were withdrawn due to election of species. Applicants submit that claim 4 has been amended herein to include the features indicated allowable, and that claim 4, 6 and 7 are now allowable and should be rejoined.

Claim 1 stands rejected under 35 USC 103(a) as being allegedly unpatentable over Yonemaru, U.S. Patent Application Publication No. US 2002/0070409 A1 in view of Majumdar et al., U.S. Patent Application Publication No. US 2004/0070047 A1. The applicants respectfully request that this rejection be withdrawn for the following reasons.

Claim 1 is amended herein to include the features indicated allowable, e.g. the claimed arrangement of a vertical and lateral junction field effect transistor (JFET) and is therefore allowable.

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Claim 1 is amended herein to include the features indicated allowable, e.g. the claimed arrangement of a vertical and lateral junction field effect transistor (JFET) and is therefore allowable.

In particular, as indicated in the Examiner's indication of Allowable Subject Matter, the applied references alone or in combination and the prior art of record fail to teach or suggest the claimed features of a vertical and lateral JFET. Applicants acknowledge that Yonemaru fails to teach or suggest a silicon carbide based vertical JFET and fails to teach or suggest a silicon carbide based lateral JFET. Yonemaru at best describes a silicon based MOSFET.

Majumdar, at best, describes a silicon carbide based MOSFET, but fails to teach or suggest a vertical or a lateral JFET individually and further fails to teach or suggest the combination of a vertical and lateral JFET.

Accordingly it is respectfully submitted that the applied art combination fails to teach or suggest all the claimed features as required. It is requested that the rejection of claim 1 be reconsidered and withdrawn.

Claims 2 and 3 stands rejected under 35 USC 103(a) as being allegedly unpatentable over Yonemaru, and Majumdar as applied above and further in view of Baliga, U.S. Patent No. 5,396,085. The applicants respectfully request that this rejection be withdrawn for the following reasons.

Claims 2 and 3, by virtue of depending from claim 1 are allowable for at least the reasons set forth herein above with regard to claim 1. It is requested that the rejection of claims 2 and 3 be reconsidered and withdrawn.

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The indication of allowability with regard to claims 5, 8 and 9 is noted with appreciation. Claim 5 is rewritten herein to include the features of claim 1 and is therefore allowable. Claims 8 and 9 are also allowable as indicated by virtue of depending from claim 5.

In view of the foregoing, the applicants respectfully submit that this application is in condition for allowance. A timely notice to that effect is respectfully requested. If questions relating to patentability remain, the examiner is invited to contact the undersigned by telephone.

Please charge any unforeseen fees that may be due to Deposit Account No. 50-1147.

Respectfully submitted,



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